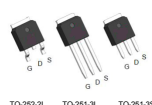
P-Channel Enhancement Mode MOSFET

Feature

- -100V/-40A
  $R_{DS(ON)}$ 42mΩ(typ.) @ V_{GS} = -10V
  $R_{DS(ON)}$ 48mΩ(typ.) @ V_{GS} = -4.5V
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

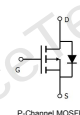
Pin Description



TO-252-2L TO-251-3L TO-251-3S

Applications

- Portable equipment and battery powered systems
- DC-DC Converters
- Motor control.



P-Channel MOSFET

Ordering and Marking Information

D	U	V	Package Code
G400P10 XYMXXXXXX	G400P10 XYMXXXXXX	G400P10 XYMXXXXXX	D: TO-252-2L U: TO-251-3L V: TO-251-3S Date Code XYMXXXXXX

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plateTermi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free require-ments of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage		-100	V
V_{GSS}	Gate-Source Voltage		±20	V
T_J	Maximum Junction Temperature		175	°C
T_{STG}	Storage Temperature Range		-55 to 175	°C
I_S	Source Current-Continuous(Body Diode)	Tc=25°C	-40	A
Mounted on Large Heat Sink				
I_{DM}	Pulsed Drain Current *	Tc=25°C	-145	A
I_D	Continuous Drain Current	Tc=25°C	-40	A
		Tc=100°C	-28.3	A
P_D	Maximum Power Dissipation	Tc=25°C	100	W
		Tc=100°C	50	W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		1.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient **		110	°C/W
E_{AS}	SinglePulsed-Avalanche Energy ***	L=0.3mH	270	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.
** Surface mounted on 1in2 FR-4 board.
*** Limited by $T_J$max , starting T_J=25°C, L = 0.3mH, VD=-80V, V_{GS} =10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG400P10LR1 Min	Typ.	Max	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V,I_{DS}= -250μA	-100	-	-	V
I_{DSS}	Drain-to-Source Leakage Current	V_{DS}= -100V,V_{GS}=0V	-	-	-1	μA
		T_J=55°C	-	-	-5	μA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}= -250μA	-1	-2	-3	V
I_{GSS}	Gate-Source Leakage Current	V_{GS}= ± 20V,V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}= -10V,I_{DS}= -20A	-	42	55	mΩ
		V_{GS}= -4.5V,I_{DS}=-20A	-	48	63	
Diode Characteristics						
V_{SD}*	Diode Forward Voltage	I_{SD}= -20A,V_{GS}=0V	-	-0.8	-1.3	V
t_{rr}	Reverse Recovery Time	I_{SD}=-20A,dIsoldt=100A/μs	-	90	-	ns
Q_{rr}	Reverse Recovery Charge		-	70	-	nC

Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG400P10LR1 Min	Typ.	Max	Unit
Dynamic Characteristics						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	-	9.4	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V,	-	5003	-	pF
C_{oss}	Output Capacitance	V_{DS}= -25V,	-	189	-	
C_{rss}	Reverse Transfer Capacitance	Frequency=1.0MHz	-	111	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}= -50V,R_{GS}=6.8Ω, I_{DS}= -20A,V_{GS}= -10V	-	22	-	ns
T_r	Turn-on Rise Time		-	26	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	74	-	
T_f	Turn-off Fall Time		-	66	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge	V_{GS} = -50V, V_{GS}= -10V, I_{DS}= -15A	-	83.1	-	nC
Q_{gs}	Gate-Source Charge		-	16.8	-	
Q_{gd}	Gate-Drain Charge		-	12	-	

Note: *Pulse test: pulse width ≤ 300us, duty cycle ≤ 2%

Typical Operating Characteristics

Figure 1: Power Dissipation

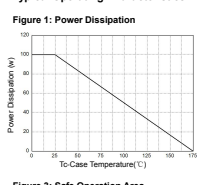


Figure 2: Drain Current

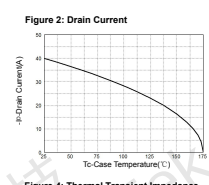


Figure 3: Safe Operation Area

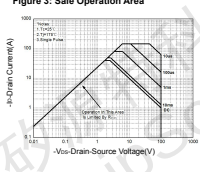


Figure 4: Thermal Transient Impedance

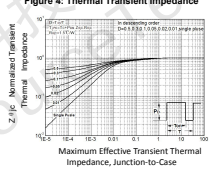


Figure 5: Output Characteristics

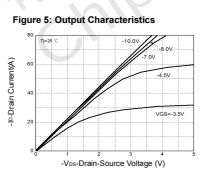
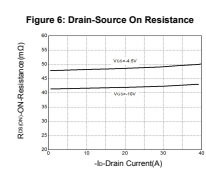


Figure 6: Drain-Source On Resistance



Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

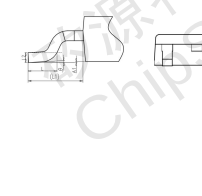


Figure 8: Source-Drain Diode Forward

Figure 9: Capacitance Characteristics

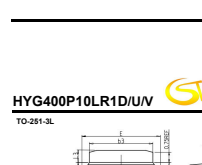
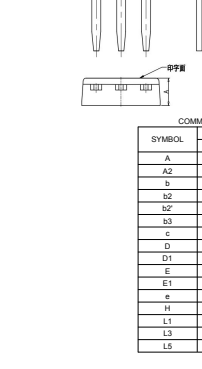
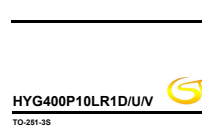


Figure 10: Gate Charge Characteristics

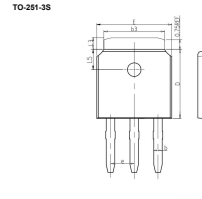
Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



Gate Charge Test Circuit and Waveforms

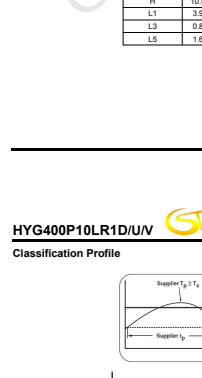


Device Per Unit

Package Type	Unit	Quantity
TO-252-2L	Tube	75
TO-251-3L	Tube	75
TO-251-3S	Tube	75

Package Information

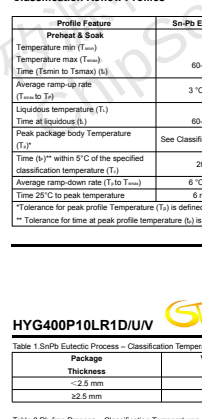
TO-252-2L



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.40
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
c	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.286BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90REF		
L2	0.51BSC		
L3	0.88	-	1.28
L4	-	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°

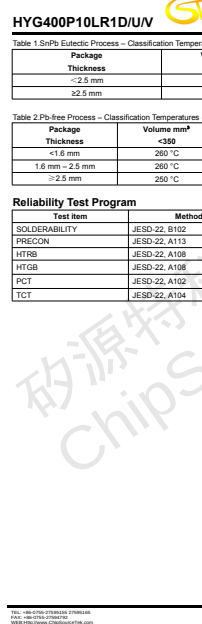
TO-251-3L



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.50
c	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.286BSC		
H	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95

TO-251-3S



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
c	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.286BSC		
H	10.00	11.22	11.44
L1	3.90	4.10	4.30
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95

Classification Profile

Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (Tsmin to Tsmax) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ 350-2000	Volume mm³ ≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
PRECON	JESD-22, A113	30°C/60%/192Hrs
HTRB	JESD-22, A108	168 Hrs/500hr/1000hr, Bias @ 150°C
HTGB	JESD-22, A108	168 Hrs/500hr/1000hr, V_{GS}=100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

TEL: +86-0755-27595333 27595166
FAX: +86-0755-27595177
WEB:http://www.ChipSourceTek.com
E-mail: Sales@ChipSourceTek.com Tony.Wang@ChipSourceTek.com